

A2
A digital switching system includes a multiplexer for multiplexing time slots from a plurality of circuits, a switching memory for storing and switching the multiplexed data for one frame period, switching control equipment for directing the interchange of the time slots stored in the switching memory in response to a switching request from an upper layer controller in a network, and a demultiplexer for demultiplexing into the plurality of circuits the time slot data as read out using address data supplied from the switching control equipment. The switching control equipment includes a control memory unit for writing connection information from the upper layer controller to either a first or a second control memory, and sequentially reading out the stored connection information in read-out order for the switching memory, and a selection unit for selecting read-out from either the first or the second control memory in response to the switching request.